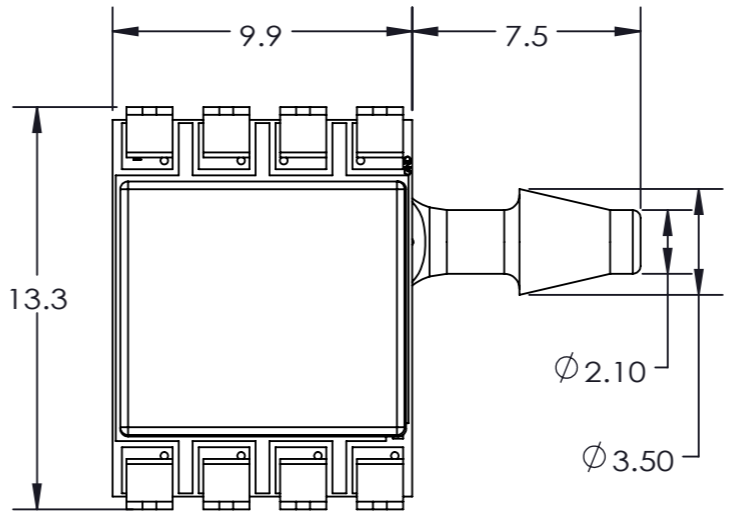
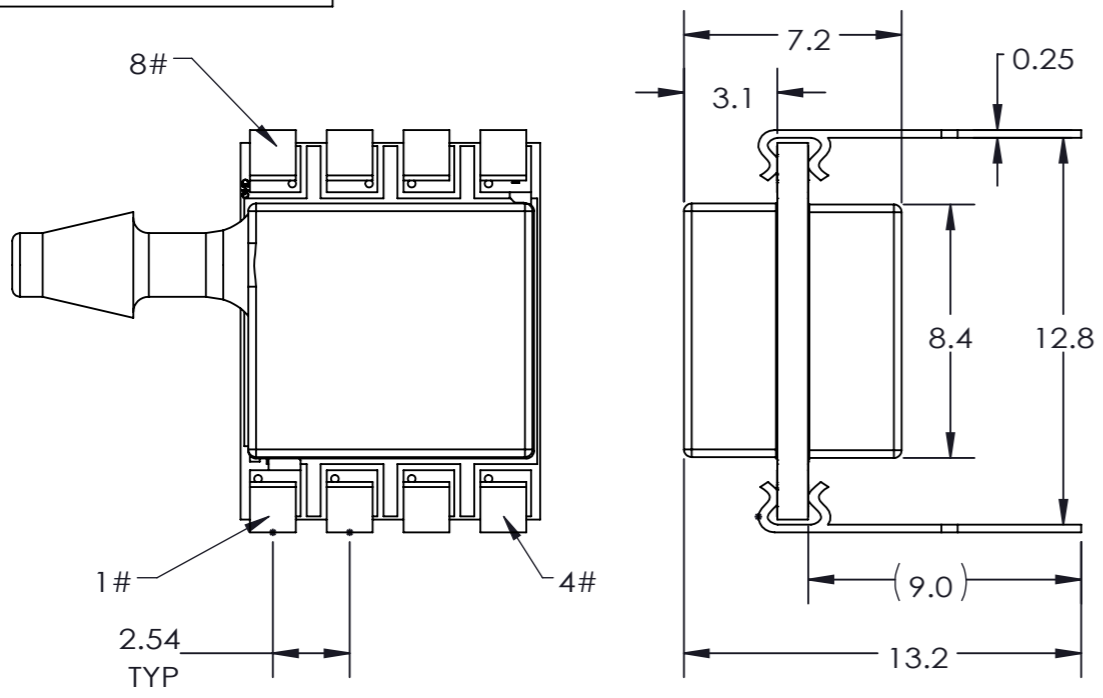


SUPPLY VOLTAGE: 3.3V  
 AMBIENT TEMPERATURE: 25°C (UNLESS OTHERWISE SPECIFIED)

PARAMETERS	MIN	TYP	MAX	UNITS	NOTES
OUTPUT	-	666	-	COUNT HEX	1,2,3
SPAN	-	3333	-	COUNT HEX	1,2,3
ACCURACY	-0.25	-	0.25	% SPAN	2
TOTAL ERROR BAND	-1	-	+1	% SPAN	3
INPUT VOLTAGE RANGE	2.70	-	5.50	V	
SUPPLY CURRENT	-	3	-	mA	
BURST PRESSURE	4.8	-	-	BarA	
LOAD RESISTANCE (R <sub>L</sub> )	10	-	-	KΩ	
LONG TERM STABILITY (OFFSET & SPAN)	-	±0.2	-	% SPAN	
COMPENSATED TEMPERATURE	0	-	+60	°C	
OPERATING TEMPERATURE	-25	-	+85	°C	
STORAGE TEMPERATURE	-25	-	+85	°C	
HUMIDITY	-	-	95	RH	
WEIGHT	-	3	-	GRAMS	
UPDATE TIME	-	0.5	-	mS	6
START TIME TO DATA READY	-	-	8.4	mS	6
SOLDER TEMPERATURE	250°C MAX 5 SEC.				

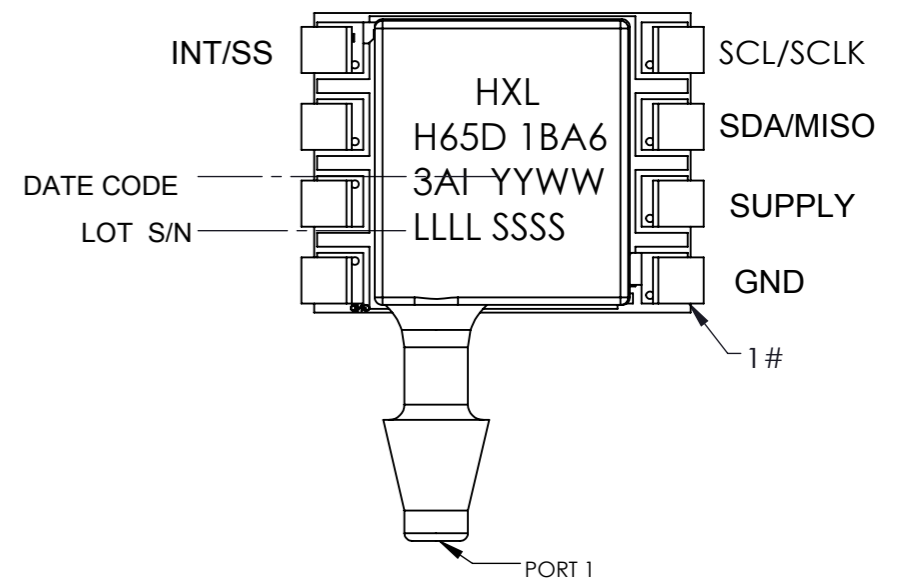
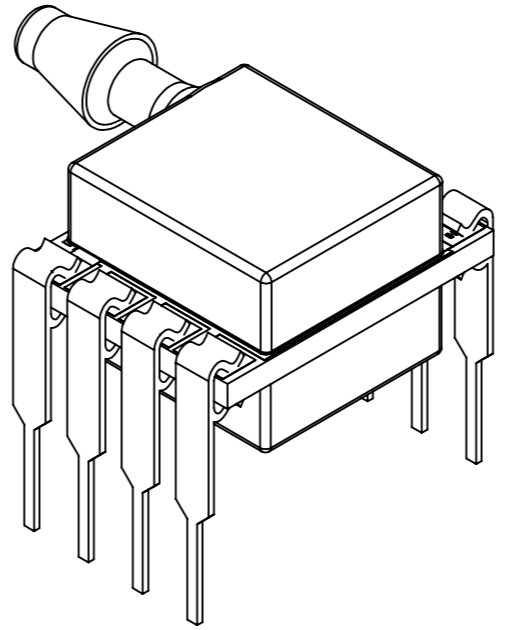
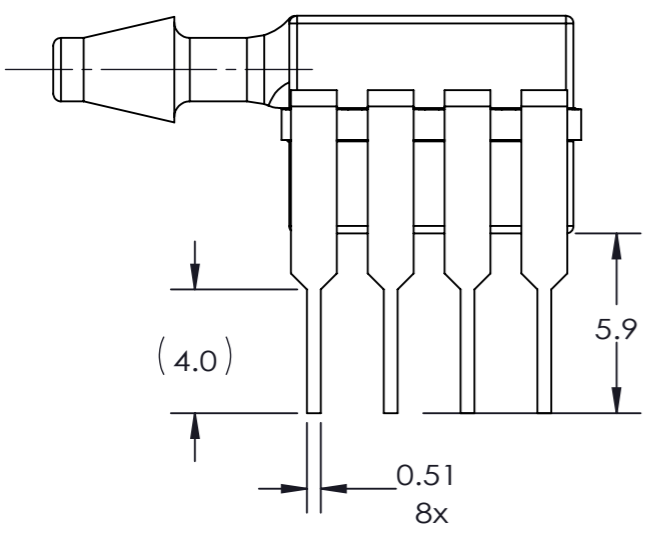
MEDIA: NON-CORROSIVE LIQUID ON PORT1 COMPATIBLE WITH CERAMIC, SILICON, RTV, GOLD AND EPOXY.

A



\*\* SEE SHEET 2 FOR NOTES \*\*

B



1# GND, 2# VSS, 3#SDA, 4# SCL


描图

描校

底图存放

签字

日期

				SPEC			 <b>华芯联科技</b> HUAXINLIAN
				mm			
修改次数 CHG	修改文件 EC NO.	签名 NAME	日期 DATE	阶段 STAGE	单件重量 WEIGHT	比例 SCALE	<b>Spec-H65D</b>
设计 DES		THIRD ANGLE PROJECTION	第三角投影法			<b>1:1</b>	
审核 AUDIT		控制 DES CONT		共 3 张 第 1 张 TOTAL SHEET 3 NO.1			UNLESS OTHERWISE SPECIFIED, TOLERANCES ARE: ANGULAR: ±3° ONE PLACE DECIMAL ±0.30 TWO PLACE DECIMAL ±0.08
工艺 TECH		图幅 SIZE	A3				
批准 APPD							

BLOCK DIAGRAM

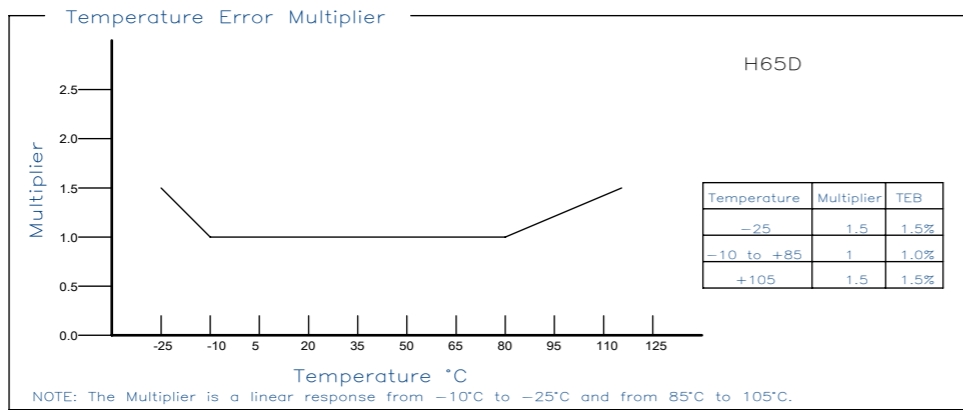
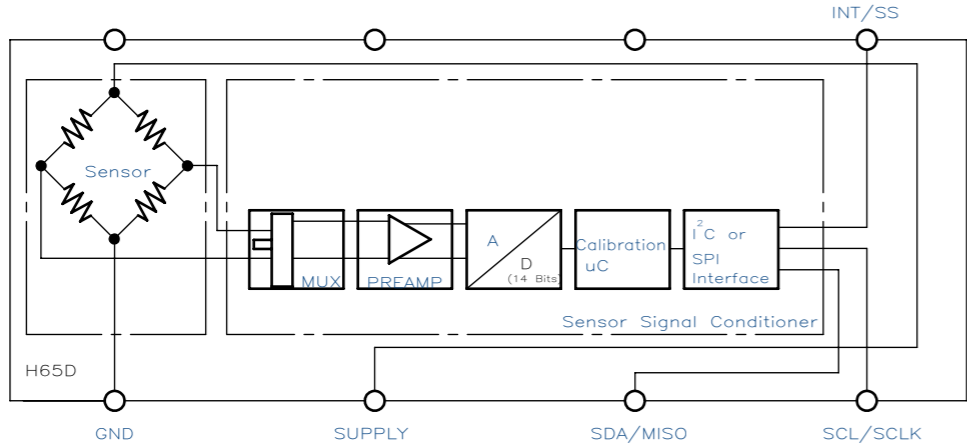


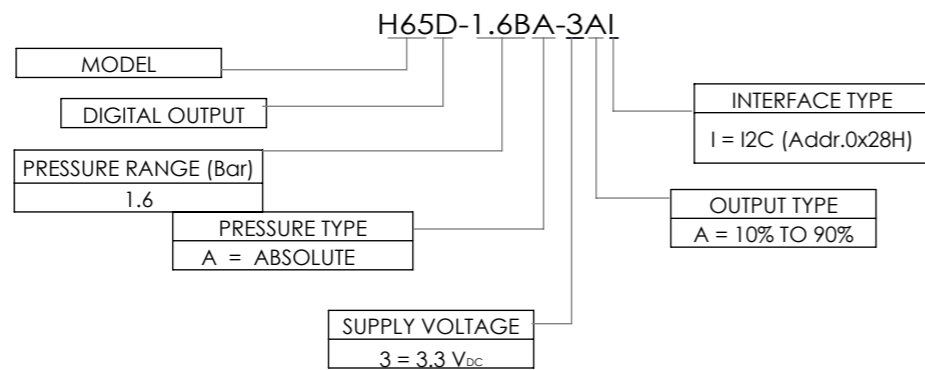
TABLE 2, PRESSURE TYPE DESCRIPTIONS

Pressure Type	Pmin	Pmax	Description
Absolute	0 BarA	+1.6BarA	Output is proportional to the difference between 0 BarA (Pmin) and pressure applied to Port 1.

NOTES

1. PROPER OPERATION REQUIRES AN EXTERNAL CAPACITOR PLACE AS SHOWN IN CONNECTION DIAGRAM. OUTPUT IS NOT RATIO METRIC TO SUPPLY VOLTAGE.
2. THE MAXIMUM DEVIATION FROM A BEST FIT STRAIGHT LINE (BFSL) FITTED TO THE OUTPUT MEASURED OVER THE PRESSURE RANGE AT 25°C. INCLUDES ALL ERRORS DUE TO PRESSURE NON LINEARITY, HYSTERESIS, NON REPEATABILITY.
3. TOTAL PRESSURE ERROR BAND INCLUDES ALL ACCURACY ERRORS, THERMAL ERRORS OVER THE COMPENSATED TEMPERATURE RANGE, SPAN AND OFFSET CALIBRATION TOLERANCES. FOR IDEAL SENSOR OUTPUT WITH RESPECT TO INPUT PRESSURE AND TEMPERATURE, REFERENCE TRANSFER FUNCTION CHARTS BELOW.
4. THE DEVIATION FROM A BEST FIT STRAIGHT LINE (BFSL) FITTED TO THE OUTPUT MEASURED OVER THE COMPENSATED TEMPERATURE RANGE.
5. FOR ERRORS BEYOND THE COMPENSATED TEMPERATURE RANGE, SEE EXTENDED TEMPERATURE MULTIPLIER CHART BELOW.
6. START TIME TO DATA READY IS THE TIME TO GET VALID DATA AFTER POR (POWER ON RESET). THE TIME TO GET SUBSEQUENT VALID DATA IS THEN SPECIFIED BY THE UPDATE TIME SPECIFICATION.
7. LONG TERM STABILITY OVER A ONE YEAR PERIOD WITH CONSTANT VOLTAGE AND TEMPERATURE.
8. PRESSURE CONNECTION: BARBED PORTS ARE DESIGNED FOR USE WITH TUBING 3/32" ID, 60-70A DUROMETER (PVC).
9. FOR DETAILS ON ENVIRONMENTAL CONDITIONS AND PRESSURE TYPE DESCRIPTIONS, REFERENCE TABLES 2 and 3

B



描图

描校

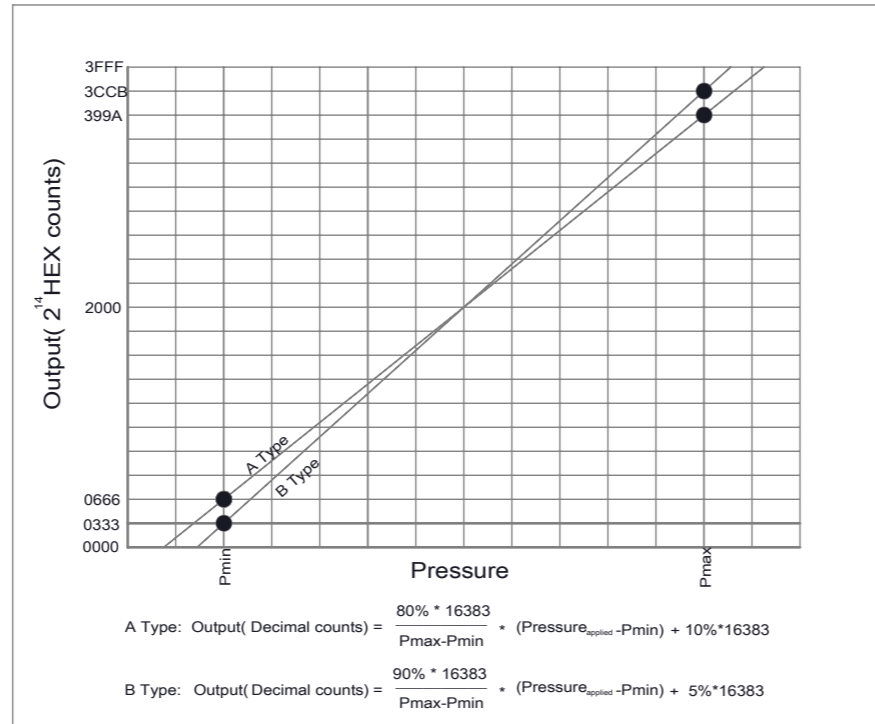
底图存放

签字

日期

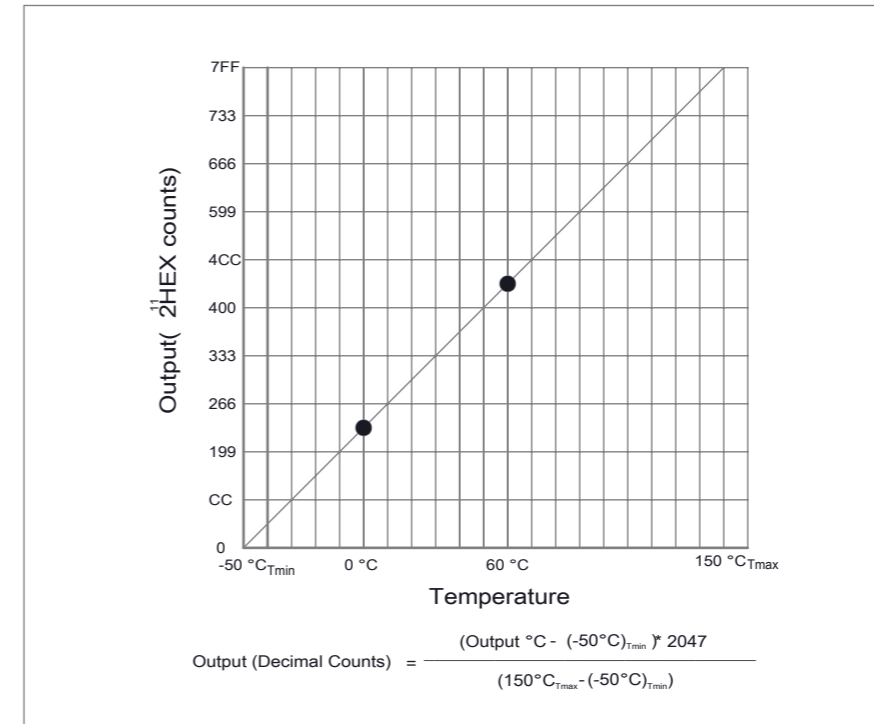
				SPEC			华芯联科技 FINAL ASSEMBLY, H65D, <b>Spec-H65D</b>
				mm			
修改次数 CHG	修改文件 EC NO.	签名 NAME	日期 DATE	阶段 STAGE	单件重量 WEIGHT	比例 SCALE	共 3 张 第 3 张 TOTAL SHEET 3 NO.3
设计 DES		THIRD ANGLE PROJECTION	第三角投影法			1:1	
审核 AUDIT		控制 DES CONT					
工艺 TECH		图幅 SIZE	A3				
批准 APPD							

Pressure Transfer Functions



% of Counts	Output Type A (Bar)	Output Type B (Bar)	Digital Counts (decimal)	Digital Counts (hex)
0	$P_{min}-(P_{max}-P_{min}) \cdot 1/8$	$P_{min}-(P_{max}-P_{min}) \cdot 5/90$	0	0 X 0000
5		$P_{min}$	819	0 X 0333
10	$P_{min}$		1638	0 X 0666
50			8192	0 X 2000
90	$P_{max}$		14746	0 X 399A
95		$P_{max}$	15563	0 X 3CCB
100	$P_{max}+(P_{max}-P_{min}) \cdot 1/8$	$P_{max}+(P_{max}-P_{min}) \cdot 5/90$	16383	0 X 3FFF

Temperature Transfer Functions

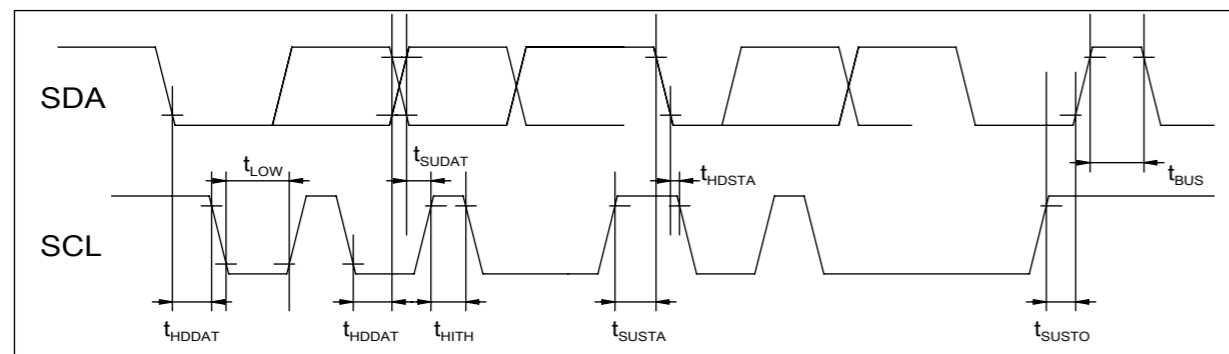


Output °C	Digital Counts (decimal)	Digital Counts (hex)
-50	0	0 X 0000
0	511	0 X 01FF
10	614	0 X 0266
25	767	0 X 02FF
50	1023	0 X 03FF
85	1381	0 X 0565
150	2047	0 X 07FF

PARAMETERS	SYMBOL	MIN	TYP	MAX	UNITS
SCLK CLOCK FREQUENCY	F <sub>SCL</sub>	100		400	KHz
START CONDITION HOLD TIME RELATIVE TO SCL EDGE	t <sub>HDSTA</sub>	0.1			µs
MINIMUM SCL CLOCK LOW WIDTH @1	t <sub>LOW</sub>	0.6			µs
MINIMUM SCL CLOCK HIGH WIDTH @1	t <sub>HIGH</sub>	0.6			µs
START CONDITION SETUP TIME RELATIVE TO SCL EDGE	t <sub>SUSTA</sub>	0.1			µs
DATA HOLD TIME ON SDA RELATIVE TO SCL EDGE	t <sub>HDDAT</sub>	0			µs
DATA SETUP TIME ON SDA RELATIVE TO SCL EDGE	t <sub>SUDAT</sub>	0.1			µs
STOP CONDITION SETUP TIME ON SCL	t <sub>SUSTO</sub>	0.1			µs
BUS FREE TIME BETWEEN STOP AND START CONDITION	t <sub>BUS</sub>	2			µs

@1 COMBINED LOW AND HIGH WIDTHS MUST EQUAL OR EXCEED MINIMUM SCL PERIOD.

I2C INTERFACE TIMING DIAGRAM



				SPEC			华芯联科技 HUAXINLIAN	
				mm			FINAL ASSEMBLY, H65D,	
修改次数 CHG	修改文件 EC NO.	签名 NAME	日期 DATE	阶段 STAGE	单件重量 WEIGHT	比例 SCALE	Spec-H65D	
设计 DES		THIRD ANGLE PROJECTION	第三角投影法			1:1		
审核 AUDIT		控制 DES CONT						
工艺 TECH		图幅 SIZE	A3	共 3 张 TOTAL SHEET 3		第 3 张 NO.3		
批准 APPD								

A

B

描图

描校

底图存放

签字

日期